

800G OSFP immersion cooling extender

P/N: GLOE-PC801-DXX for OSFP

P/N: GLRE-PC801-DXX for RHS



Features

- ◆ Compliant OSFP MSA
- ◆ Typical insertion loss less 8dB@26.56GHz with 0.3m length
- ◆ 100ohm differential impedance system
- ◆ 3.3V power supply
- ◆ I2C R/W function
- ◆ Status indicators with LED
- ◆ 3.3V/0.85W fan and heat sink for contact cooling
- ◆ Low EMI radiation and crosstalk
- ◆ RoHS compliant(lead free)

Applications

- ◆ Extend 800G OSFP transceiver/AOC for liquid immersion link environment
- ◆ Protect device OSFP SMT connector
- ◆ provide I2C R/W and some status indicators with LED

Description

Gigalight can offer rich experience of immersion solution, that includes different form and speed transceivers/AOC/product. Gigalight 800G OSFP immersion cooling extender (GLOE-PC801-DXX) is an important part of liquid immersion solution, normal OSFP form transceiver/AOC can be used for immersion environment with this product. This product include extender cage, cable, OSFP housing three parts, the cable length can be customized no more than 0.3m for extension, that can avoid the optical lens/engine/interface exposure to the liquid indirectly.

In addition, this product can provide I2C read/write, also can show the status indicators with LED for low speed electrical hardware pins. When insertion and removal frequently, this product can effectively protect the OSFP SMT connector of switch/NIC.

Liquid cooling Advantage

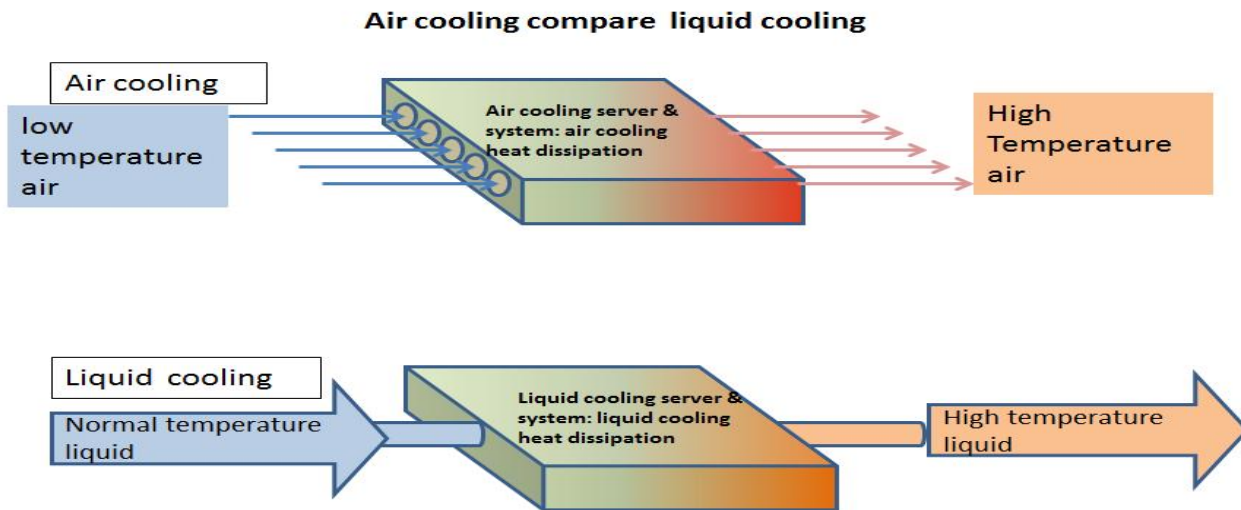


Figure 1. Liquid cooling advantage

As the requirement of data traffic keeping growth and the heat flux emitted by datacenter internal chips increases constantly, traditional air cooling methods are under pressure. Liquid cooling technologies removes the heat more efficiently with dielectric fluids that have high heat capacity to improve the efficiency of energy in datacenter.

Gigalight solved the lack of optical transceivers which perform reliability in immersion even liquid immersion depth up to 10m, the Liquid cooling optical series transceiver is suitable for liquid cooling server & system, this series product are compatible with fluorinated liquid and mineral oils well.

Immersion cooling extender can also be a important role in liquid immersion solution, existing normal OSFP form transceiver/AOC can be adapted for immersion indirectly.

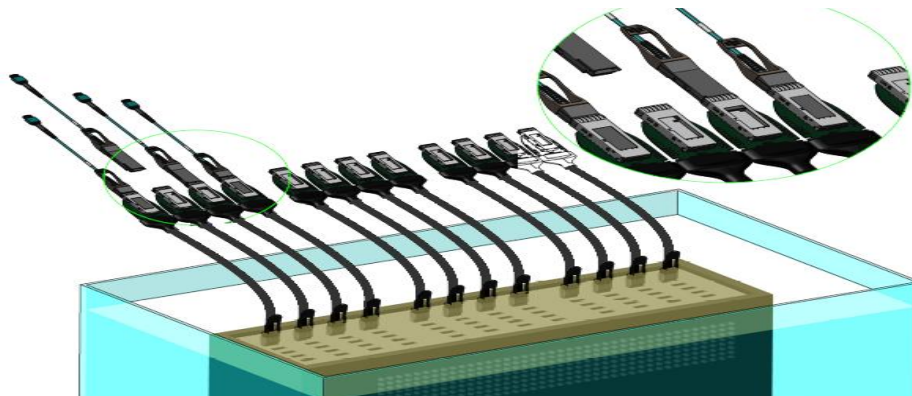


Figure 2 Immersion cooling extender under liquid

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Storage Temperature	T_s	-20	85	°C
Case Operating Temperature	T_c	0	70	°C
Humidity (non-condensing)	Rh	5	95	%

Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Supply Voltage	Vcc	3.13		3.47	V
Support Power Dissipation	Pm			16	w
Operating Case Temperature	T_c	0		70	°C
Baud Rate per Lane	fd		53.125		GBaud/s

Main Part assembly

P1:Extender contact

P2:Extender cable

P3:Extender housing



Figure 3 OSFP extender main part assembly

Electrical pinout

Top Side (viewed from top)

60	GND	
59	TX1p	
58	TX1n	
57	GND	
56	TX3p	
55	TX3n	
54	GND	
53	TX5p	
52	TX5n	
51	GND	
50	TX7p	
49	TX7n	
48	GND	
47	SDA	
46	VCC	
45	VCC	
44	INT/RSTn	
43	GND	
42	RX8n	
41	RX8p	
40	GND	
39	RX6n	
38	RX6p	
37	GND	
36	RX4n	
35	RX4p	
34	GND	
33	RX2n	
32	RX2p	
31	GND	

Module Card Edge

Bottom Side (viewed from bottom)

	GND	1
	TX2p	2
	TX2n	3
	GND	4
	TX4p	5
	TX4n	6
	GND	7
	TX6p	8
	TX6n	9
	GND	10
	TX8p	11
	TX8n	12
	GND	13
	SCL	14
	VCC	15
	VCC	16
	LPWn/PRSn	17
	GND	18
	RX7n	19
	RX7p	20
	GND	21
	RX5n	22
	RX5p	23
	GND	24
	RX3n	25
	RX3p	26
	GND	27
	RX1n	28
	RX1p	29
	GND	30

Electrical Pin-out Details

Pin#	Symbol	Description	Logic	Direction	Plug Sequence	Notes
1	GND	Ground			1	
2	TX2p	Transmitter Data Non-Inverted	CML-I	Input from Host	3	
3	TX2n	Transmitter Data Inverted	CML-I	Input from Host	3	
4	GND	Ground			1	
5	TX4p	Transmitter Data Non-Inverted	CML-I	Input from Host	3	
6	TX4n	Transmitter Data Inverted	CML-I	Input from Host	3	
7	GND	Ground			1	
8	TX6p	Transmitter Data Non-Inverted	CML-I	Input from Host	3	
9	TX6n	Transmitter Data Inverted	CML-I	Input from Host	3	
10	GND	Ground			1	
11	TX8p	Transmitter Data Non-Inverted	CML-I	Input from Host	3	
12	TX8n	Transmitter Data Inverted	CML-I	Input from Host	3	
13	GND	Ground			1	
14	SCL	2-wire Serial interface clock	LVCNOS-I/O	Bi-directional	3	Open-Drain with pull-up resistor on Host
15	VCC	+3.3V Power		Power from Host	2	
16	VCC	+3.3V Power		Power from Host	2	
17	LPWn/PRSn	Low-Power Mode / Module Present	Multi-Level	Bi-directional	3	See pin description for required circuit
18	GND	Ground			1	
19	RX7n	Receiver Data Inverted	CML-O	Output to Host	3	
20	RX7p	Receiver Data Non-Inverted	CML-O	Output to Host	3	
21	GND	Ground			1	
22	RX5n	Receiver Data Inverted	CML-O	Output to Host	3	
23	RX5p	Receiver Data Non-Inverted	CML-O	Output to Host	3	
24	GND	Ground			1	
25	RX3n	Receiver Data Inverted	CML-O	Output to Host	3	
26	RX3p	Receiver Data Non-Inverted	CML-O	Output to Host	3	
27	GND	Ground			1	
28	RX1n	Receiver Data Inverted	CML-O	Output to Host	3	
29	RX1p	Receiver Data Non-Inverted	CML-O	Output to Host	3	
30	GND	Ground			1	
31	GND	Ground			1	
32	RX2p	Receiver Data Non-Inverted	CML-O	Output to Host	3	

Pin#	Symbol	Description	Logic	Direction	Plug Sequence	Notes
33	RX2n	Receiver Data Inverted	CML-O	Output to Host	3	
34	GND	Ground			1	
35	RX4p	Receiver Data Non-Inverted	CML-O	Output to Host	3	
36	RX4n	Receiver Data Inverted	CML-O	Output to Host	3	
37	GND	Ground			1	
38	RX6p	Receiver Data Non-Inverted	CML-O	Output to Host	3	
39	RX6n	Receiver Data Inverted	CML-O	Output to Host	3	
40	GND	Ground			1	
41	RX8p	Receiver Data Non-Inverted	CML-O	Output to Host	3	
42	RX8n	Receiver Data Inverted	CML-O	Output to Host	3	
43	GND	Ground			1	
44	INT/RSTn	Module Interrupt / Module Reset	Multi-Level	Bi-directional	3	See pin description for required circuit
45	VCC	+3.3V Power		Power from Host	2	
46	VCC	+3.3V Power		Power from Host	2	
47	SDA	2-wire Serial interface data	LVC MOS-I/O	Bi-directional	3	Open-Drain with pull-up resistor on Host
48	GND	Ground			1	
49	TX7n	Transmitter Data Inverted	CML-I	Input from Host	3	
50	TX7p	Transmitter Data Non-Inverted	CML-I	Input from Host	3	
51	GND	Ground			1	
52	TX5n	Transmitter Data Inverted	CML-I	Input from Host	3	
53	TX5p	Transmitter Data Non-Inverted	CML-I	Input from Host	3	
54	GND	Ground			1	
55	TX3n	Transmitter Data Inverted	CML-I	Input from Host	3	
56	TX3p	Transmitter Data Non-Inverted	CML-I	Input from Host	3	
57	GND	Ground			1	
58	TX1n	Transmitter Data Inverted	CML-I	Input from Host	3	
59	TX1p	Transmitter Data Non-Inverted	CML-I	Input from Host	3	
60	GND	Ground			1	

Figure 4. Extender housing OSFP SMT connector pin

Mechanical Dimensions

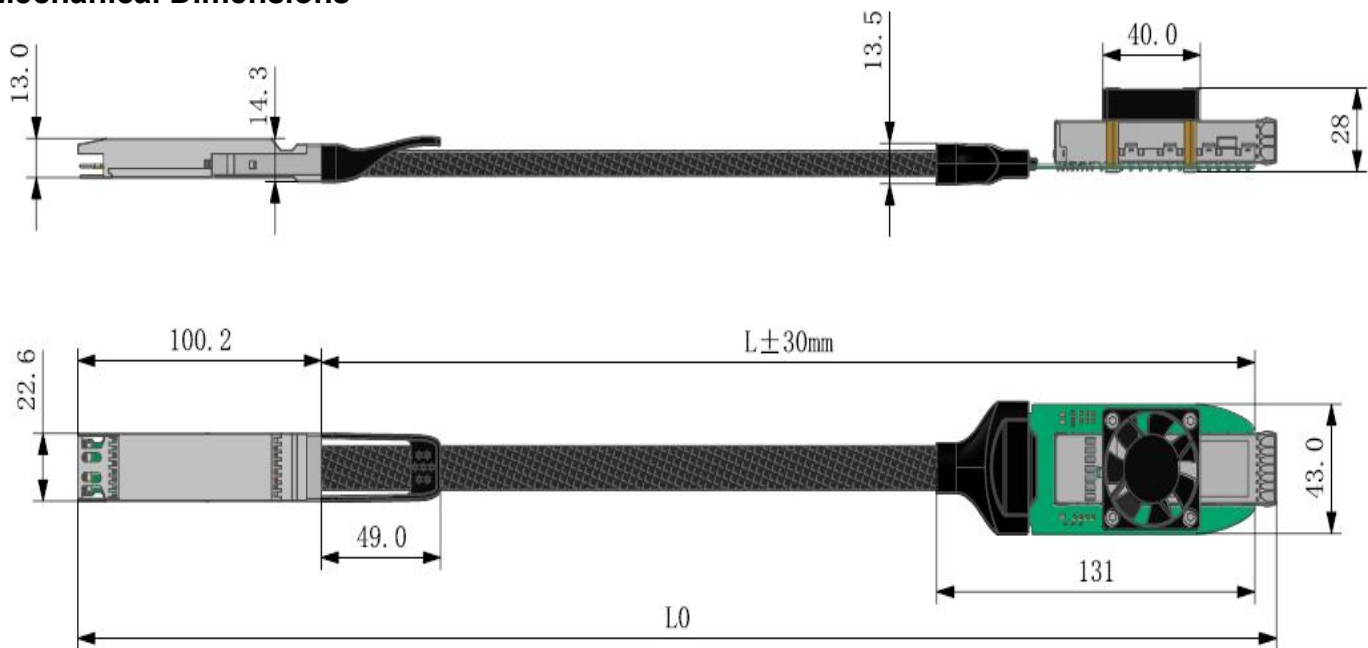


Figure 5. OSFP Mechanical Specifications

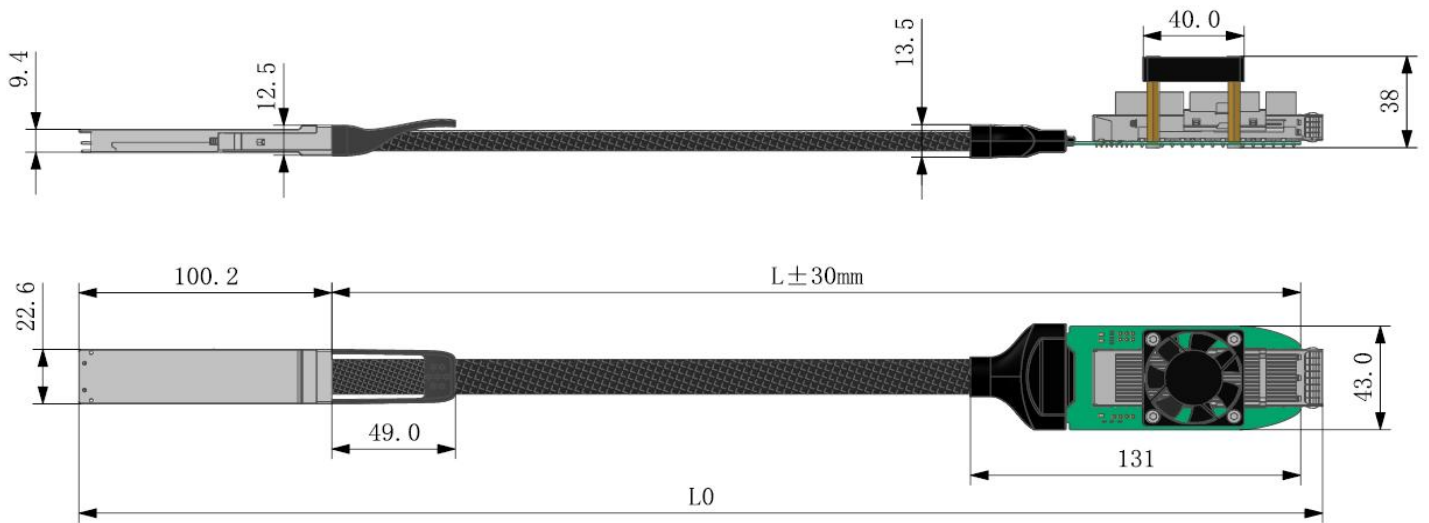


Figure 6. OSFP-RHS Mechanical Specifications

Extender housing pin

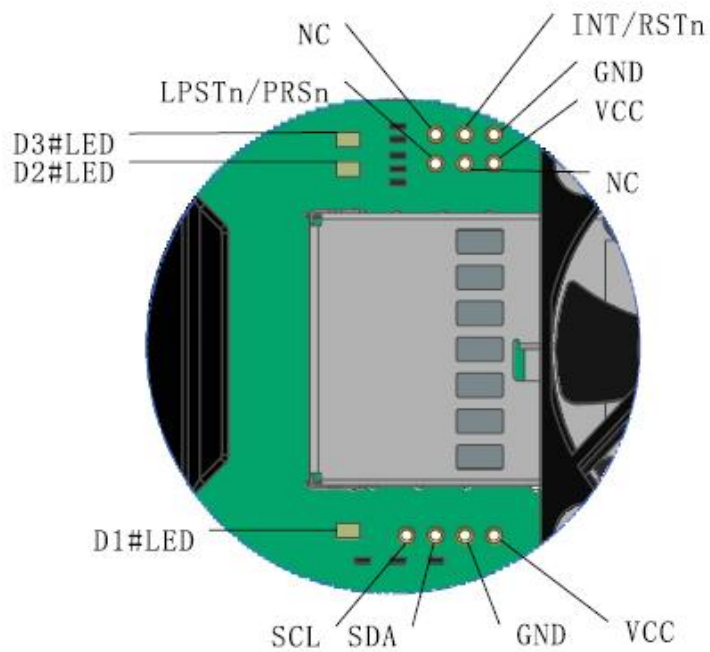


Figure 7. Extender housing pin

Regulatory Compliance

Gigalight's 800G OSFP immersion cooling extender meet the requirements of the following standards:

Feature	Standard
Electrical Safety	EN 62368-1: 2014 IEC 62368-1:2014 UL 62368-1:2014
Environmental protection	Directive 2011/65/EU with amendment(EU)2015/863
CE EMC	EN55032: 2015 EN55035: 2017 EN61000-3-2:2014 EN61000-3-3:2013
FCC	FCC Part 15, Subpart B; ANSI C63.4-2014

Ordering information

Part Number	Length	Description
GLOE-PC801-D03	30cm	800G OSFP extender with high speed cable, with PET jacket , with fan,0.3 meter length as of Figure 5.
GLRE-PC801-D03	30cm	800G OSFP-RHS extender with high speed cable, with PET jacket , with fan, 0.3 meter length as of Figure 5.

- 1.The length (meter) and wire gage of GLOE-PC801-DXX is decimal and can be customizable
- 2.The extender housing also can be customizable as OSFP RHS form.
- 3.Length as “L” of Mechanical Specifications

Important Notice

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Revision History

Revision	Date	Description
V0	28-Nov-2025	Advance Release.